

RECEIVED  
CENTRAL FAX CENTER

JAN 07 2004

**facsimile**  
TRANSMITTAL

OFFICIAL

Date: January 7, 2004

No. of Pages: 7 (including this cover sheet)

Fax No.: (703) 872-9306

**PLEASE DELIVER THE FOLLOWING PAGES IMMEDIATELY TO:**

Name: Commissioner of Patents

Art Unit: 2816

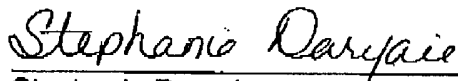
Examiner: Tuan Thieu Lam

Phone: (703) 305-3791

From: Jun-Young E. Jeon  
Reg No. 43,693Re: Application No. 10/621,005  
Filed July 16, 2003  
Entitled SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT  
(I/O) CIRCUIT

File: 50716/DBP/B600

I HEREBY CERTIFY THAT THESE PAPERS (Information Disclosure Statement, Form PTO/SB/08A/B, Transmittal of Drawings, "Annotated Marked-Up Drawing," and "Replacement Sheet") ARE BEING FACSIMILE TRANSMITTED TO THE UNITED STATES PATENT AND TRADEMARK OFFICE ON **January 7, 2004**.



Stephanie Daryaie

\*Correspondence: Information Disclosure Statement, Form PTO/SB/08A/B, Transmittal of Drawings, "Annotated Marked-Up Drawing," and "Replacement Sheet"

For Office Services Use Only  
Return Fax to Stephanie Daryaie

Christie, Parker & Hale, LLP  
350 West Colorado Boulevard  
Post Office Box 7068  
Pasadena, CA 91109-7068  
626-795-9900  
Fax: 626-577-8800

**confidential**

The information in this transmission is confidential and is intended only for the use of the individual or entity named above. If the reader of this message is not the intended recipient, you are hereby notified that any dissemination, distribution or copying of this communication is prohibited. If you have received this communication in error, please notify us immediately by telephone collect, and return the original message to us at the above address via U.S. mail. We will reimburse you for postage. Thank you.

JAN 07 2004

OFFICIAL

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on January 7, 2004.

*Stephanie Daryaic*  
Stephanie Daryaic

Applicant : Janardhanan S. Ajit  
Application No. : 10/621,005  
Filed : July 16, 2003  
Title : SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT  
(I/O) CIRCUIT

Grp./Div. : 2816  
Examiner : Tuan Thieu Lam

Docket No. : 50716/DBP/B600

INFORMATION DISCLOSURE STATEMENT  
37 CFR § 1.97(b)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
January 7, 2004

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B listing a reference that is known to applicant. A copy of the listed reference is enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

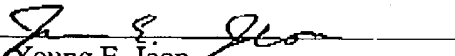
It is respectfully requested that the listed reference be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application.

**Application No. 10/621,005**

Applicant also requests that an initialed copy of FORM PTO/SB/08A/B be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By   
Jun-Young E. Jeon  
Reg. No. 43,693  
626/795-9900

JEJ/sd

Enclosures: PTO/SB/08A/B, w/reference  
SD PASS43921.1 \*-01/7/04 10:11 AM

<b>FORM PTO/SB/08A/B (10-01)</b> Substitute for PTO-1449A/B  <b>INFORMATION DISCLOSURE</b>  <b>STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)	<b>Attorney Docket Number</b>	50716/PAN/B600
	<b>Application Number</b>	10/621,005
	<b>Filing Date</b>	July 16, 2003
	<b>Applicant(s)</b>	Janardhanan S. Ajit
	<b>Group Art Unit</b>	2816
	<b>Examiner Name</b>	Tuan Thieu Lam

U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>2</sup> (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
		US 6,362,652 B1	03-26-2002	Oner et al.

FOREIGN PATENT DOCUMENTS					
EXAMINER INITIALS	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>1</sup> - Number <sup>2</sup> - Kind Code <sup>3</sup> (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>4</sup> (✓)

OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

SD PAS543923.1-\*01/7/04 10:12 AM

EXAMINER SIGNATURE	DATE CONSIDERED
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at www.pto.gov or MPEP 901.4. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English Language Translation is attached.	

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

JEJ/sd